	Type	Hits	Search Text	DBs
1	BRS	6105	ellis.xp.	USPAT
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3	BRS	368	ellis.xp. and (rich.xp. or richard.xp.)	USPAT
4	BRS	16	(ellis.xp. and (rich.xp. or richard.xp.)) and FPGA	USPAT
5	BRS	1	6021490.pn.	USPAT
6	BRS	1	6021490.pn.	USPAT
7	BRS	4	6021490.URPN.	USPAT
8	BRS	0	ellis.xp. and (rich.xp or richard.xp)	USPAT
9	BRS	79	(FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell))) same ((determin$3 or decid$3 or check$3) with (reconfigur$3 or status))	USPAT
10	BRS	23	(FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell))) same ((minimiz$3 or reduc$3 or limit$3) near2 (reconfigur$6))	USPAT
11	BRS	1	4761755.pn.	USPAT
12	BRS	1	5128559.pn.	USPAT
13	BRS	0	5142469.pn	USPAT
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15	BRS	1	5943242.pn.	USPAT
16	BRS	1	4489857.pn.	USPAT
17	BRS	1	4591979.pn.	USPAT
18	BRS	1	5043978.pn.	USPAT
19	BRS	22	"6127908"	USPAT
20	BRS	1	6127908.pn.	USPAT
21	BRS	1	5583450.pn.	USPAT
22	BRS	1	6021490.pn.	USPAT
23	BRS	1	5535406.pn.	USPAT

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24	BRS	1	5570040.pn.	USPAT
25	BRS	1	5497498.pn.	USPAT
26	BRS	1	5485104.pn.	USPAT
27	BRS	1	5410723.pn.	USPAT
28	BRS	186	712/229.ccls.	USPAT
29	BRS	212	712/226.ccls.	USPAT
30	BRS	25	717/172.ccls.	USPAT
31	BRS	97	717/173.ccls.	USPAT
32	BRS	65	717/171.ccls.	USPAT
33	BRS	252	(load or store) with ((control or configuration) near2 program) with controller	USPAT
34	BRS	20	((load or store) with ((control or configuration) near2 program) with controller) and FIFO	USPAT
35	BRS	160	(FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell))) with FIFO	USPAT
36	BRS	106	(bypassing or by-passing or (by adj passing)) near2 (FIFO or queue or (input adj buffer))	USPAT
37	BRS	0	((bypassing or by-passing or (by adj passing)) near2 (FIFO or queue or (input adj buffer))) with (FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell)))	USPAT
38	BRS	0	((bypassing or by-passing or (by adj passing)) near2 (FIFO or queue or (input adj buffer))) same (FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell)))	USPAT
39	BRS	0	((bypassing or by-passing or (by adj passing)) near2 (FIFO or queue or (input adj buffer))) and(FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell)))	USPAT
40	BRS	380	(bypass$3 or by-pass$3 or (by adj pass$3)) near2 (FIFO or queue or (input adj buffer))	USPAT

	Type	Hits	Search Text	DBs
41	BRS	29	(bypassing or by-passing or (by adj passing)) near2 (FIFO or queue or (input adj buffer))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
42	BRS	39	(bypass$3 or by-pass$3 or (by adj pass$3)) near2 (FIFO or queue or (input adj buffer)) and (FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	0	reconfigur$3 near2 based near2 (source or instruciton or event)	USPAT
44	BRS	919	(FPGA or PGA or PLD or (reconfig adj (logic or core or processor or cell))) with interface	USPAT
45	BRS	6	(reconfigur$3 or configur$3) near2 based near2 (source or instruciton or event)	USPAT
46	BRS	36	(reconfigur$3 or configur$3) near2 (based or basis or function) near2 (source or instruciton or event)	USPAT
47	BRS	1	5889982.pn.	USPAT
48	BRS	1	6021490.pn.	USPAT
49	BRS	4	6021490.URPN.	USPAT
50	BRS	1	"5128559".PN.	USPAT
51	BRS	1	rrann	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
52	BRS	2	5684980.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
53	BRS	2	5802290.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
54	BRS	115276 32	US 2002/0,188,832	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
55	BRS	20	188,832	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
56	BRS	2	"188832"	US-PGPUB
57	BRS	0	188832.pn.	US-PGPUB
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60	BRS	0	2002.pd	US-PGPUB

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63	BRS	1	20020188832.pn.	US-PGPUB
64	BRS	0	6182206.pn	USPAT
65	BRS	1	6182206.pn.	USPAT
66	BRS	5	meonske	USPAT
67	BRS	1	5802290.pn.	USPAT
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